

EF

Notice of Allowability	Application No.	Applicant(s)	
	10/672,990	LEE, JAE JIN	
	Examiner	Art Unit	
	Linh M. Nguyen	2816	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed 02/25/05.
2. ☒ The allowed claim(s) is/are 1-8.
3. ☐ The drawings filed on _____ are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☒ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☒ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date 03/17/05.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

DETAILED ACTION

This is a reply to the Applicant's amendment submitted on 02/25/2005.

Allowable Subject Matter

1. Claims 1-8 are allowed.

Reasons for Allowance

2. The following is an examiner's statement of reasons for allowance:

The closest prior art fails to disclose or fairly suggest:

a) A delay lock loop circuit, in which a divider unit outputs a first divided signal when it is detected that the pulse width of an external input clock is wider than the reference set value, and outputs a second divided signal when it is detected that the pulse width of the external input clock is shorter than a reference set value, as called for in claim 1.

b) A delay lock loop circuit, in which when the period of an output signal of a buffer means does not exceed a first delay time, a detection means outputs second logic level, and a divider means outputs a signal having a frequency obtained by dividing the frequency of the output signal of the buffer means into $\frac{1}{2}^{M+1}$, in combination with the remaining claimed limitations, as called for in claim 3; and

c) A delay lock loop circuit, in which a third delay unit for delaying an output signal of a second delay unit, in combination with the remaining claimed limitations, as called for in claim 6.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

Art Unit: 2816

fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Remarks

a) Upon receiving this Notice of Allowability the Applicant must provide the followings:

Drawings:

Including changes of Figures 1 and 9 to correct typographical errors, changing "CIRCULT" to --CIRCUIT-- in items 20, 60, and 80.

b) A telephone call was made to the Applicant's representative, Mr. W. William Park, on 03/17/05 to clarify the intention of canceling claim 2, on line 5, at page 9 under the "Remarks/Arguments" section, Mr. Park indicated that that was an inadvertent mistake and all claims 1-8 are pending in the instant application as shown in the first line, last paragraph at page 13 and also in the "Amendments To The Claims" section starting on page 5.

Inquiry

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Linh M. Nguyen whose telephone number is (571) 272-1749. The examiner can normally be reached on Alternate Fri, Monday - Thursday from 7:00 to 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2816

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Linh M. Nguyen
Primary Examiner
Art Unit 2816

LMN

A handwritten signature in black ink, appearing to read 'Linh M. Nguyen', with a long horizontal line extending to the right.